

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicants:** Wing K Luk, et al.

**Examiner:** Bernstein, Allison

**Serial No.:** 10/735,061

**Group:** Art Unit 2824

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**Docket:** YOR920030136US1 (8728-621)

**For:** **GATED DIODE MEMORY CELLS**

**Mail Stop AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313

**RESPONSE UNDER 37 C.F.R. 1.116**

Examiner:

This reply is in response to the Office Action dated February 20, 2008. Please consider the following remarks.